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# Transputer Reference Manual Inmos Limited

**inmos limited occam® 2 reference manual** - inmos ltd, occam 2 reference manual jackson, m.a., system development johnston, h., learning to program jones, c.b., systematic software development using vdm ... the occam reference manual serves to provide a single reference, and definition of the language occam. **ims b012 user guide and reference manual - transputer** - specification (inmos technical note 29) and module motherboard architecture (inmos technical note 49) manuals. for information on the transputer itself, refer to the transputer reference manual. the ims b012 is designed to be compatible with standard inmos software such as the transputer development system (tds) and the module motherboard software (mms2). **ims b008 user guide and reference manual - transputer** - cation dual-in-line transputer modules (trams), published as inmos technical note 29, and the module motherboard architecture manual. for information on the transputer itself, refer to the transputer reference manual. the ims b008 is designed to be compatible with the transputer development system (tds) and the module motherboard software (mms2). **inmos tn51 - simpler real-time programming with the transputer** - inmos manufactures a range of high performance microprocessors, called ... this technical note describes some aspects of timers on the transputer, using occam. it introduces the basics of the occam language and then goes on to ... occam language can be found in the occam reference manual [1]. 8. 2.2 timers in occam **performance improvement with the inmos dx305 occam 2 toolset** - performance improvement with the inmos dx305 occam 2 toolset ... larly from the language and libraries reference manual. the document describes the layout of code and data in memory for programs ... transputer. the manual is divided into two sections; 'basics' which **4 mbyte htram - classiccmp** - inmos products are supported worldwide through sgs-thomson sales offices and authorized distributors. 1.10 references 1 htram specification, inmos ltd 1994 2 t9000 transputer hardware reference manual, inmos ltd 1993 3 ims dx394 t9000 ansi c toolset datasheet, inmos ltd 1994 4 ims dx395 t9000 occam 2 toolset datasheet, inmos ltd 1994 **n88-23236 - nasa** - a transputer is a microcomputer with its own local memory and with links that can be used to connect one transputer to another transputer. a typical member of the transputer family is a single-chip very large scale integration (vlsi) device that contains a processor, memory, and serial links for point-to-point communication between transputers.

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